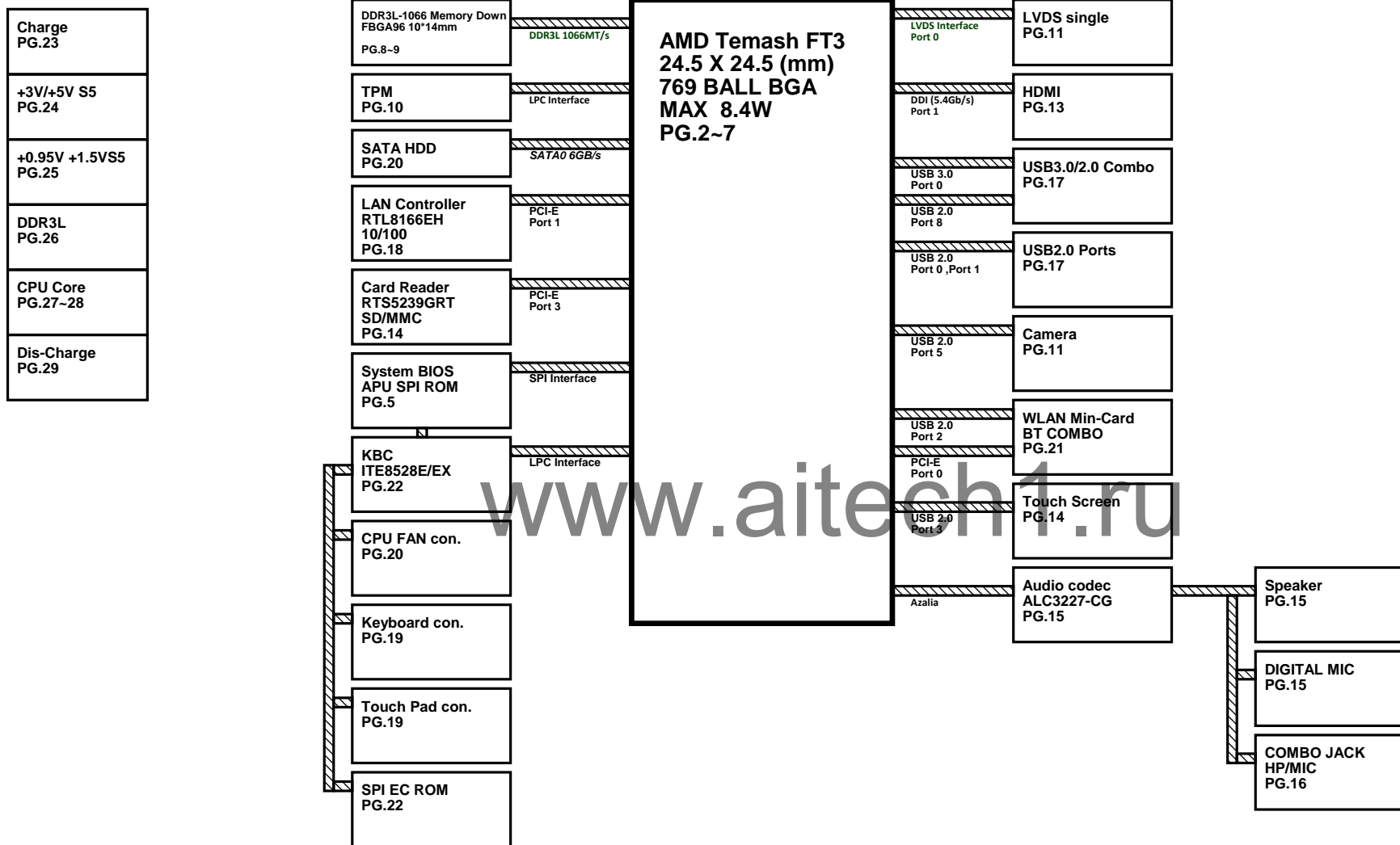


Y02 (HP Mario) AMD Temash Platform Block Diagram

01



Stackup

TOP

GND

IN1

IN2

VCC

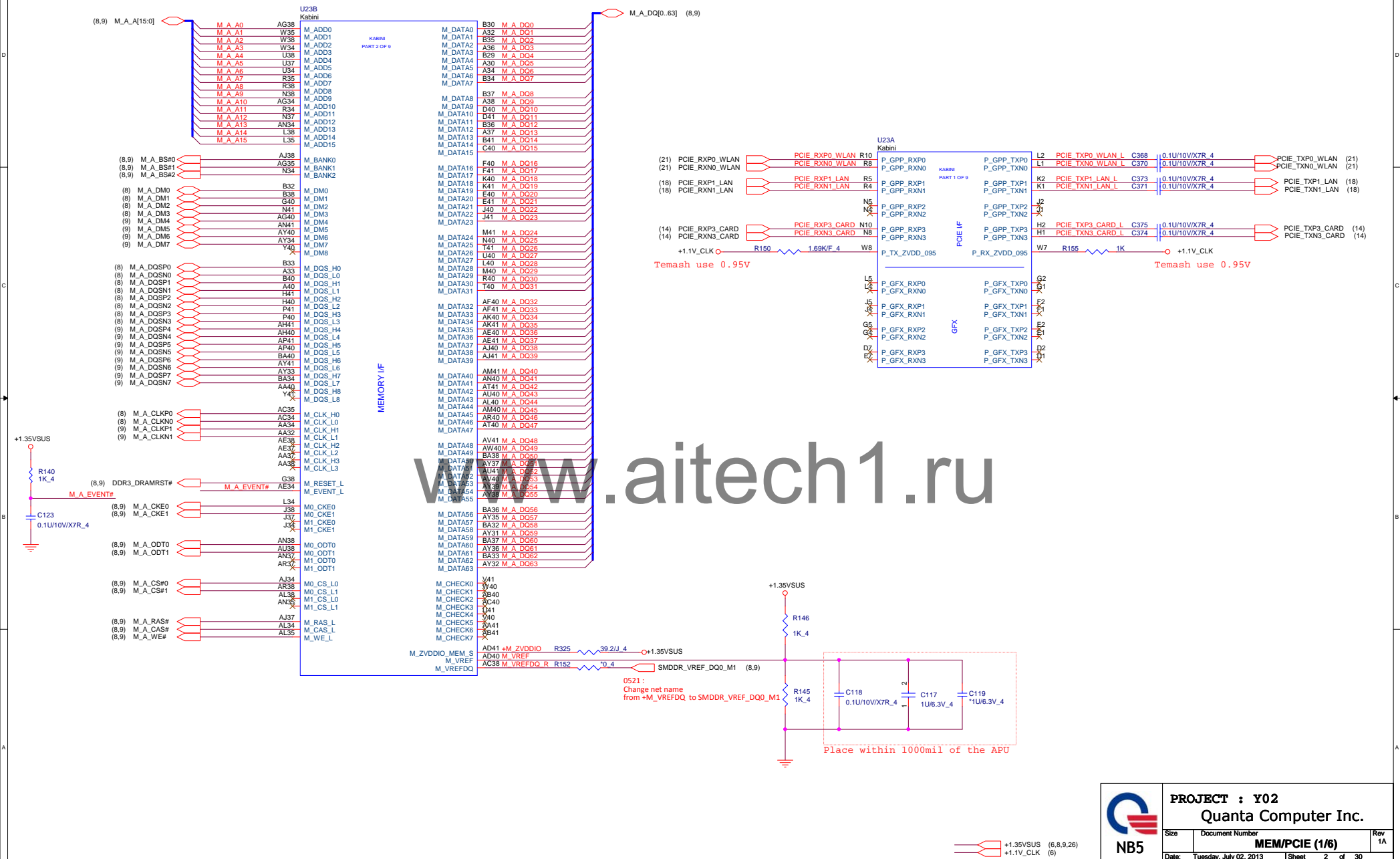
BOT

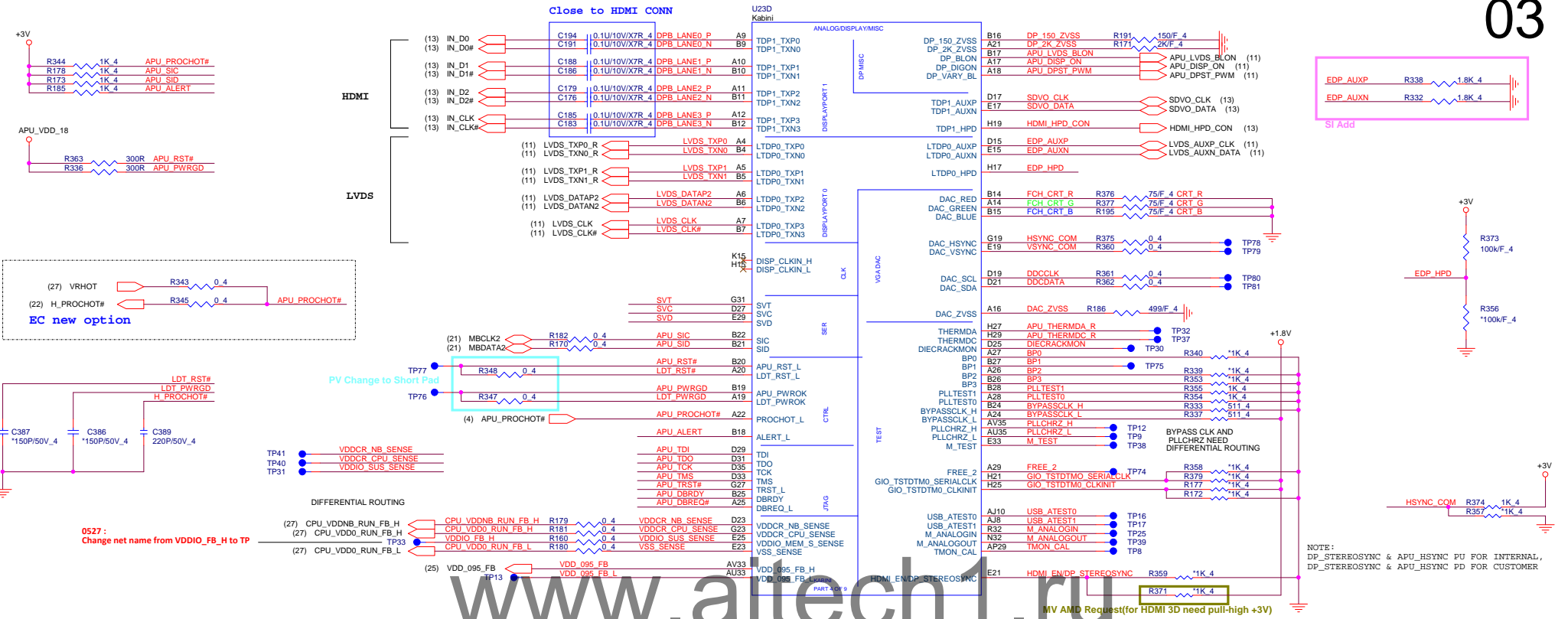


PROJECT : Y02
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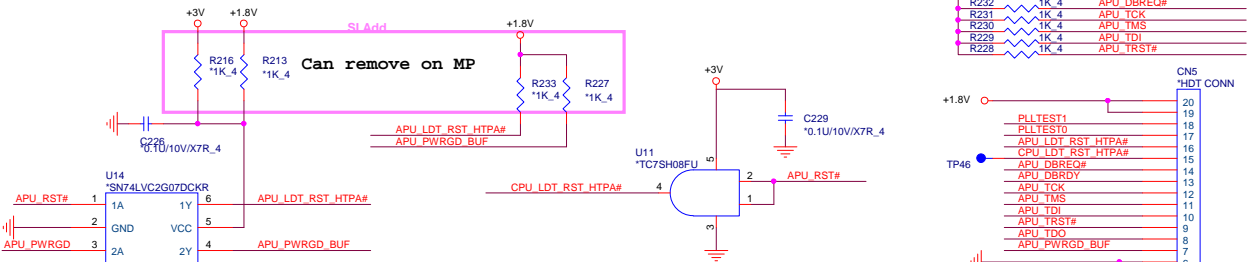
Size Custom Document Number
BLOCK DIAGRAM

Date: Tuesday, July 02, 2013 Sheet 1 of 30 Rev 1A

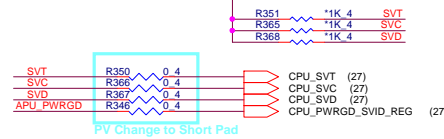




HDT(Hardware Tool) Connector



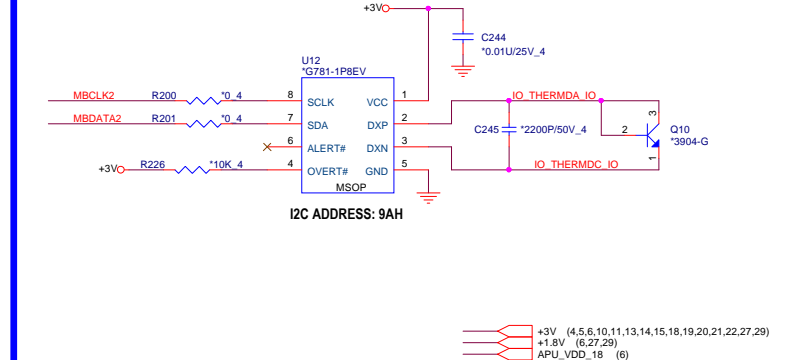
Serial VID



VFIX MODE VID Override table (VDD)

SVC	SVD	Boot Voltage
0	0	1.1V
0	1	1.0V
1	0	0.9V
1	1	0.8V

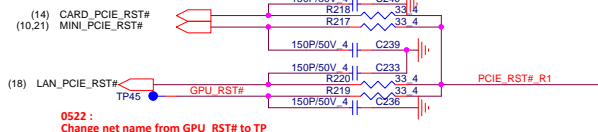
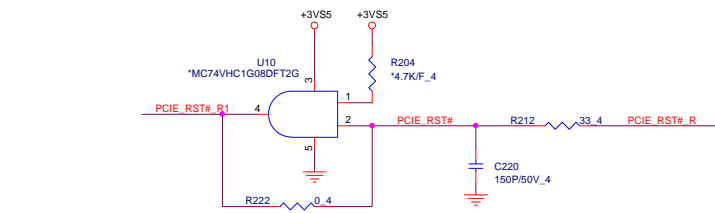
Local Thermal Sensor



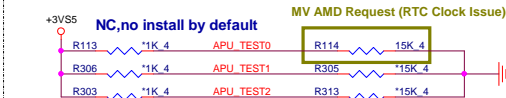
PROJECT : Y02
Quanta Computer Inc.

Size Document Number **DIS/MI (2/6)** Rev 1A

Date: Tuesday, July 02, 2013 **Sheet** 3 of 30

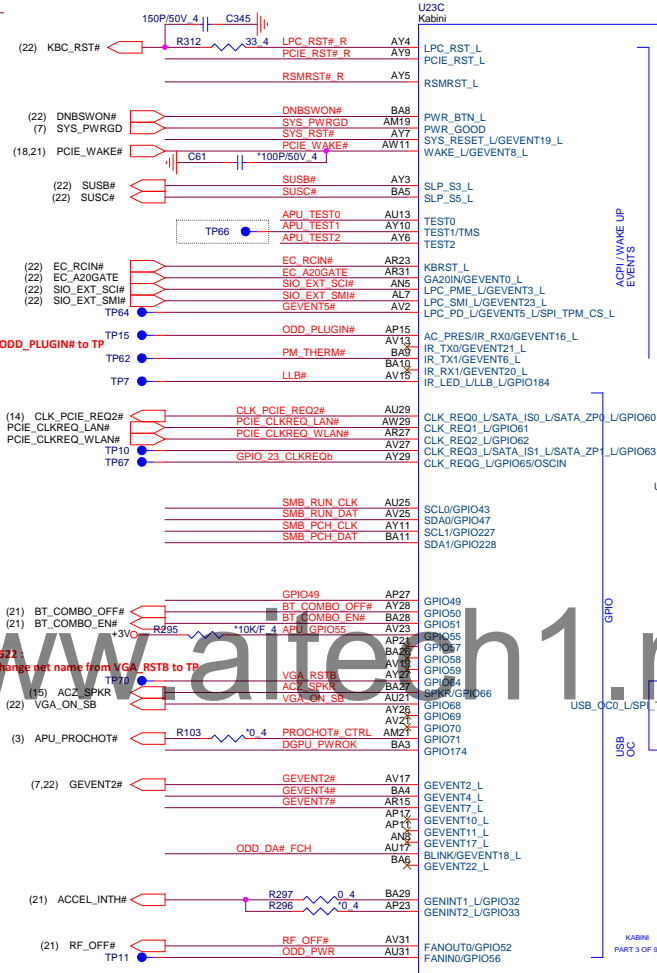
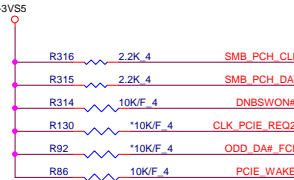
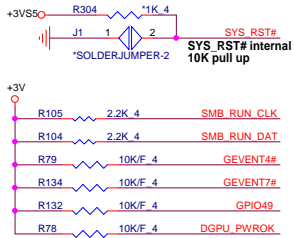


0522 :
Change net name from GPU_RST# to TP



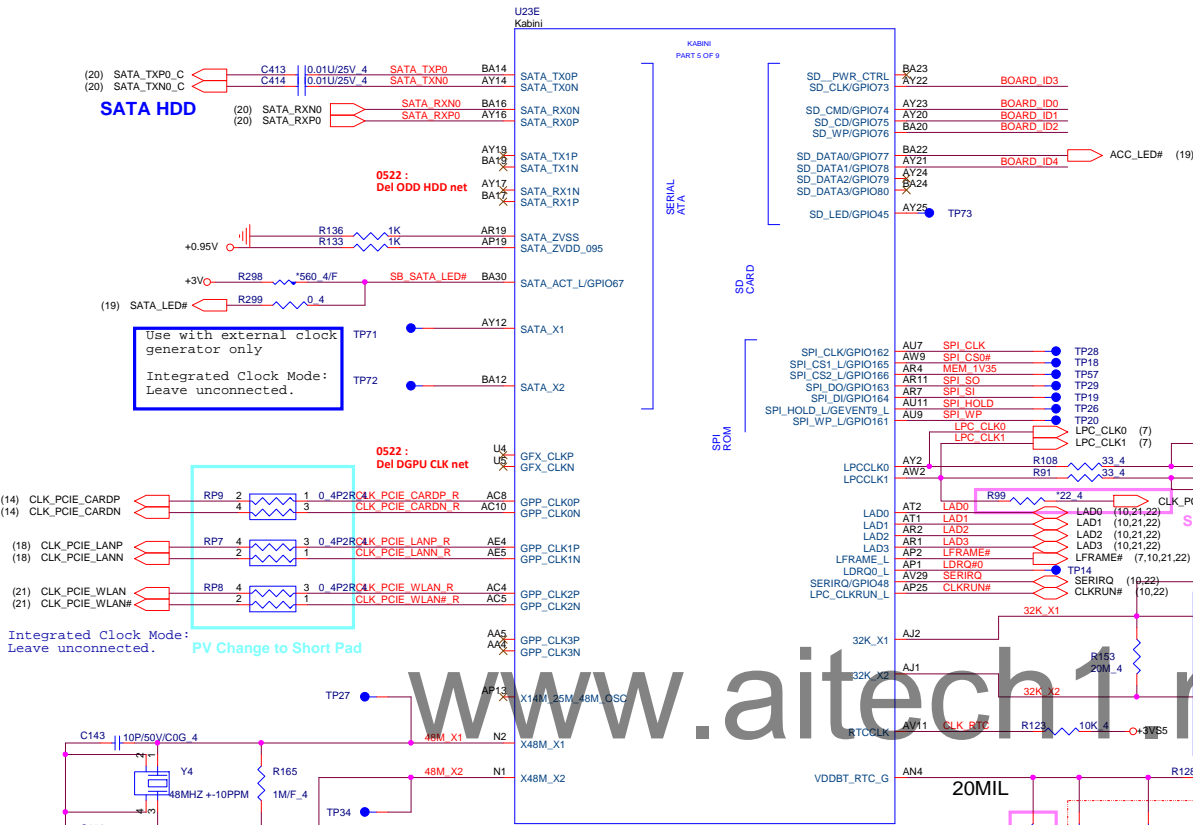
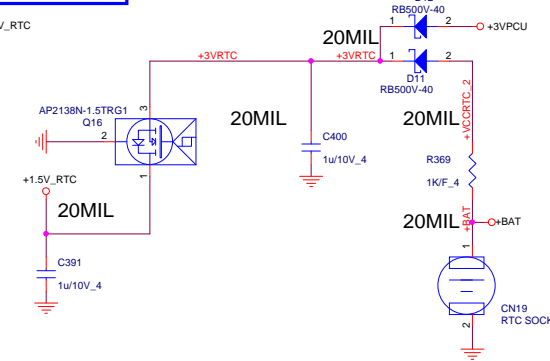
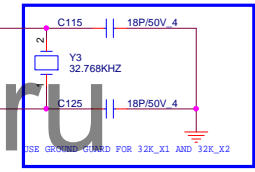
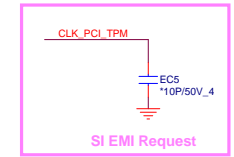
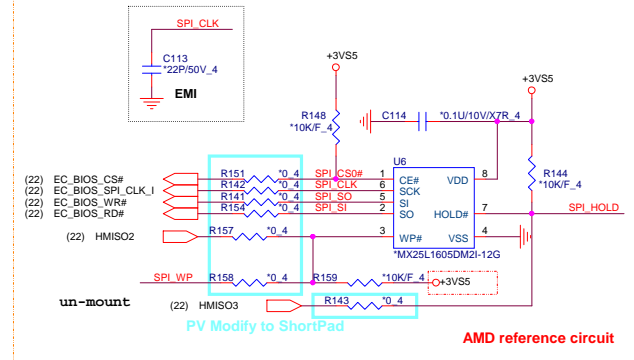
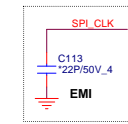
0522 :
Change net name from ODD_PLUGIN# to TP

TEST2	TEST1	TEST0	Description
0	0	0	FCH TAP accessible from APU when TAPEN is asserted FCH JTAG pins are overloaded for multiple functions, in this configuration the FCH JTAG are used as non-JTAG pins
0	0	1	Reserved
0	1	X	Reserved
1	TMS	0	FCH JTAG multi-function pins are configured as JTAG pins, in this configuration the FCH TAP can be accessed from FCH JTAG pins
1	TMS	1	Use on ATE only Yuba JTAG enabled



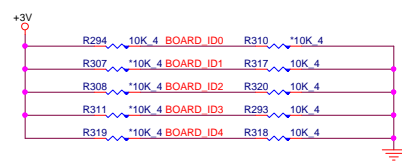
APU SPI ROM


Vender	Size	P/N
	2M	
	2M	
Socket		DFHS08FS023

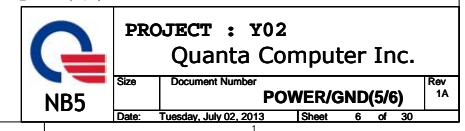


BOARD ID SETTING

Model	BOARD_ID0	BOARD_ID1	BOARD_ID2	BOARD_ID3	BOARD_ID4
10.1" UMA	0	0	0	0	0
Elpida (TN)	0	0	0	0	0
Hynix (TG)	1	0	0	0	0
Micron (TF)	0	1	0	0	0



- 
 +3VTC (13)
 +3V (3,4,6,10,11,13,14,15,18,19,20,21,22,27,29)
 +BAT (13)
 +3VS5 (4,6,7,10,12,13,21,22,24,25,29)
 +0.95V (6,25)
 +3VPCU (13,19,21,22,23,24)

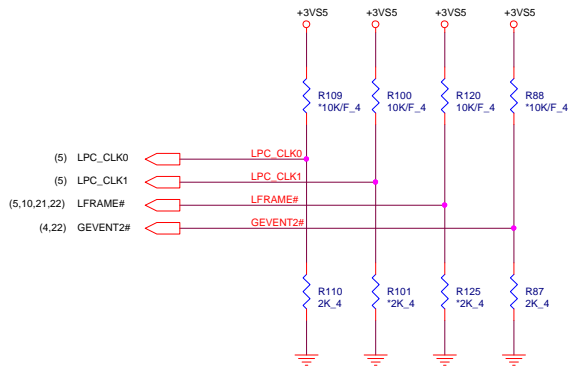




STRAPS PINS

07

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

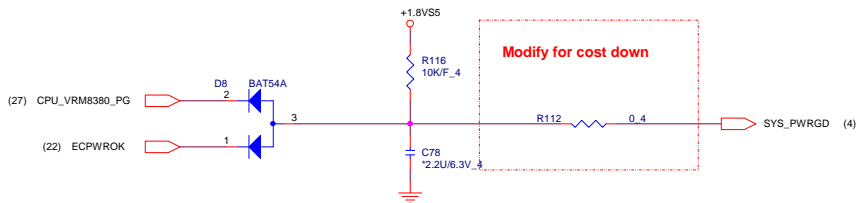


REQUIRED STRAPS


	LPC_CLK0	LPC_CLK1	LFRAME#	GEVENT2#
PULL HIGH	BOOT FAIL TIMER ENABLED	CLKGEN ENABLED DEFAULT	SPI ROM DEFAULT	1.8V SPI ROM
PULL LOW	BOOT FAIL TIMER DISABLED DEFAULT	CLKGEN DISABLED	LPC ROM	3.3V SPI ROM DEFAULT

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SYS_PWRGD

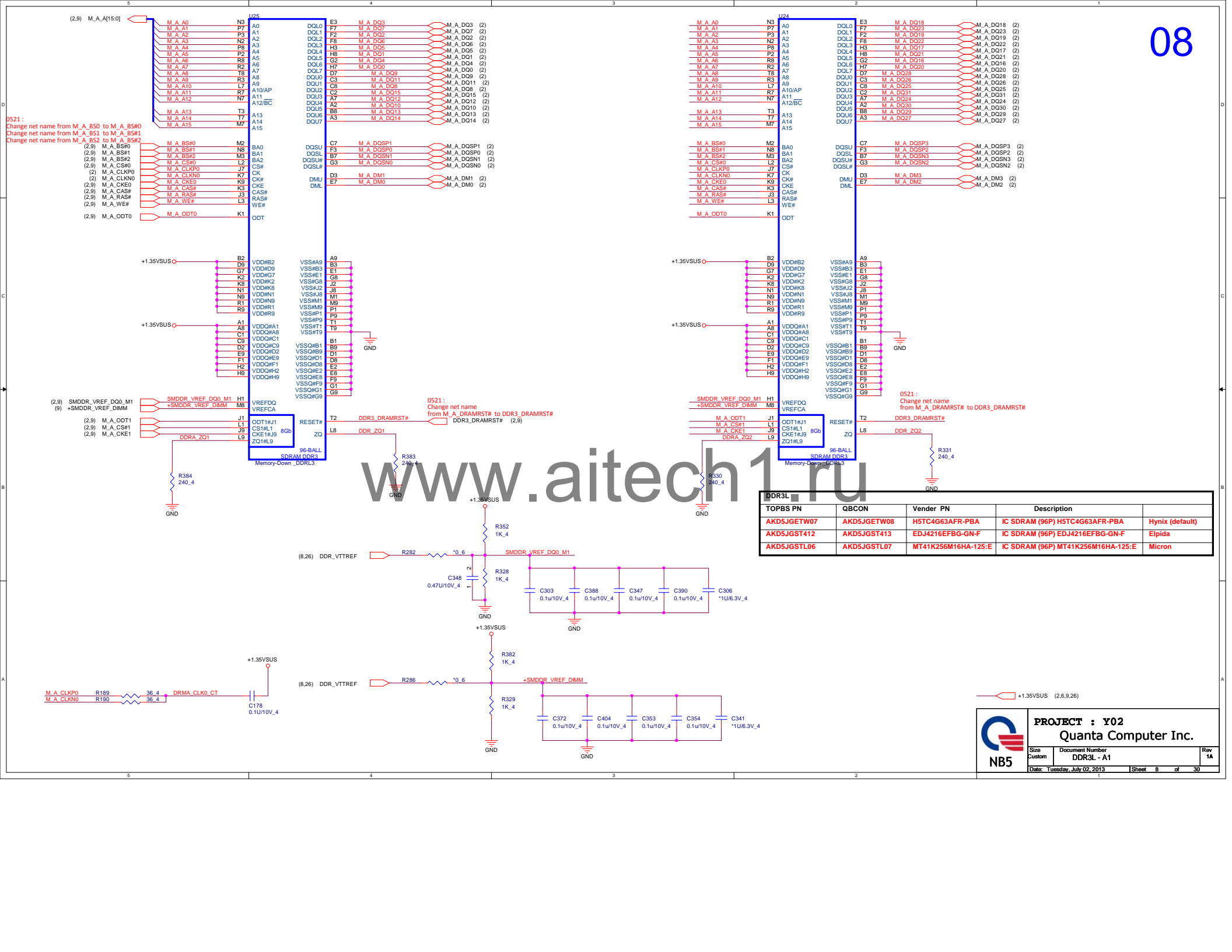


+3V (3,4,5,6,10,11,13,14,15,18,19,20,21,22,27,29)
+3VS5 (4,5,6,10,12,13,21,22,24,25,29)
+1.8VS5 (4,6,26,27,29)

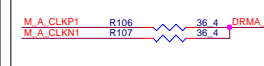
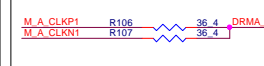


PROJECT : Y02
Quanta Computer Inc.

Size	Document Number	Rev
	STRAP (6/6)	1A
Date:	Tuesday, July 02, 2013	Sheet 7 of 30



TOPBS PN	QBCON	Vender PN	Description	
AKD5JGETW07	AKD5JGETW08	HSTC4C63AFR-PBA	IC SDRAM (96P) HSTC4C63AFR-PBA	Hynix (default)
AKD5JGST412	AKD5JGST413	EDJ4216FBG-GN-F	IC SDRAM (96P) EDJ4216FBG-GN-F	Elpida
AKD5JGSTL06	AKD5JGSTL07	MT41K256M16HA-125-E	IC SDRAM (96P) MT41K256M16HA-125-E	Micron

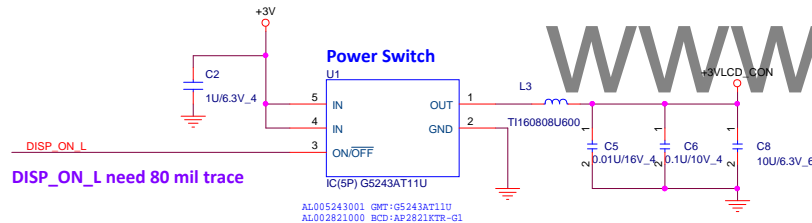
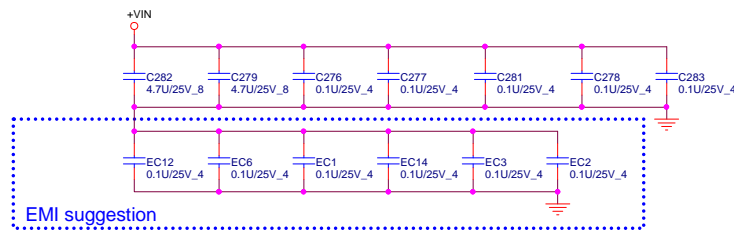
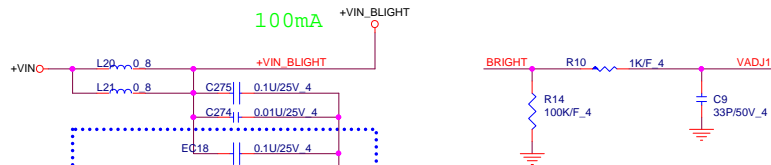
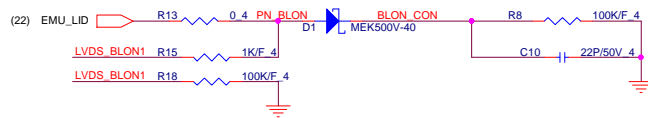


Component	Value
C366	2 0.22u/6.3V 4
C381	2 0.22u/6.3V 4
C350	2 0.22u/6.3V 4
C365	2 0.22u/6.3V 4
C360	2 0.22u/6.3V 4
C361	2 0.22u/6.3V 4
C352	2 0.22u/6.3V 4
C351	2 0.22u/6.3V 4
C356	2 0.22u/6.3V 4
C363	2 0.22u/6.3V 4
C364	2 0.22u/6.3V 4
C362	2 0.22u/6.3V 4
C397	2 0.22u/6.3V 4
C401	2 0.22u/6.3V 4
C399	2 0.22u/6.3V 4
C402	2 0.22u/6.3V 4
C357	2 0.22u/6.3V 4
C355	2 0.22u/6.3V 4

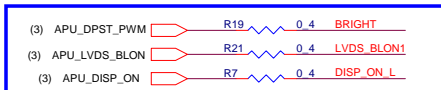
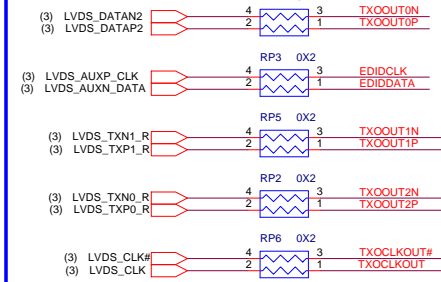


(3,4,5,6,11,13,14,15,18,19,20,21,22,27,29) +3V
(4,5,6,7,12,13,21,22,24,25,29) +3VS5

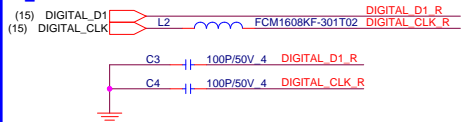
LID Switch



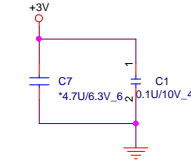
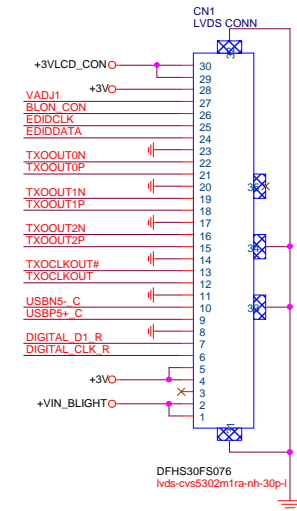
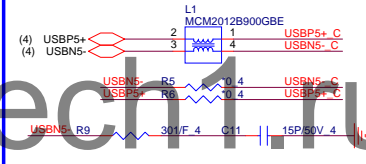
LVDS SINGLE



D_MIC

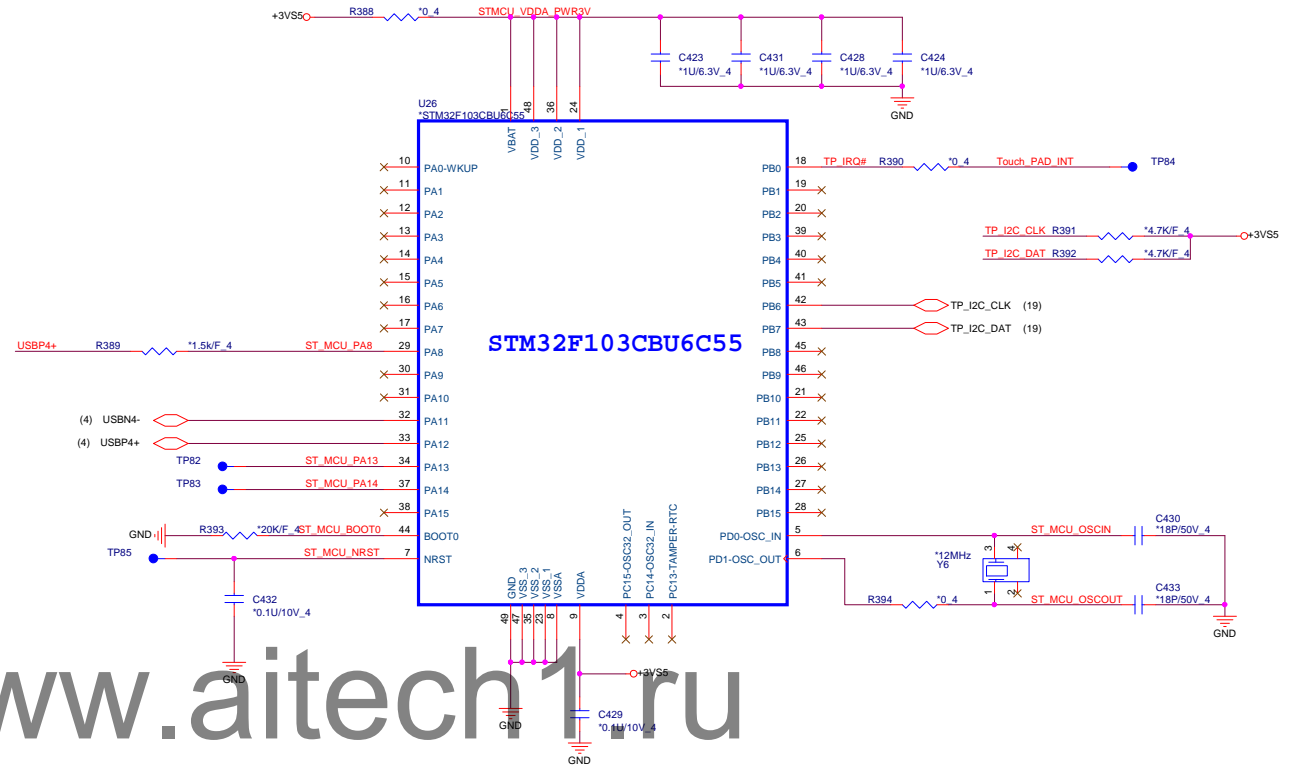


USB CAMERA

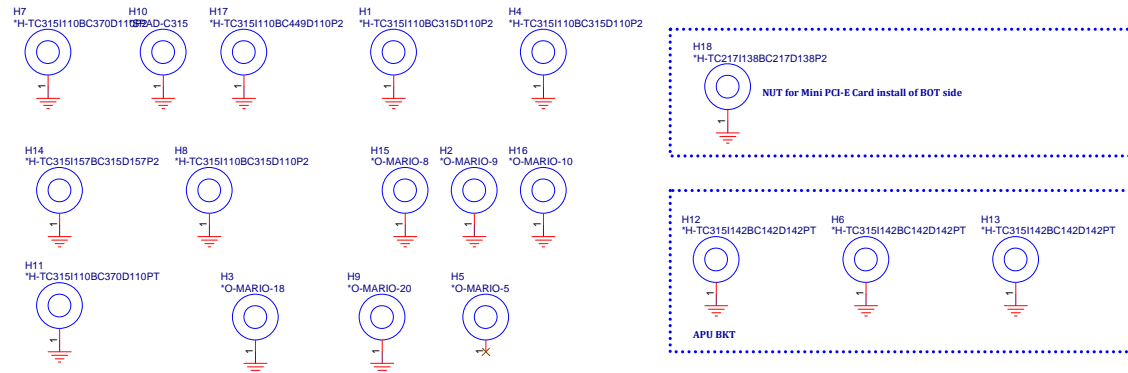





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Touch PAD I2C

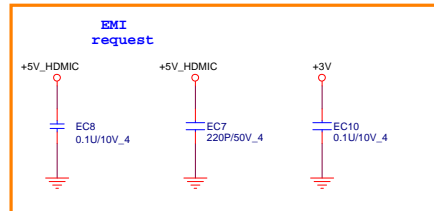
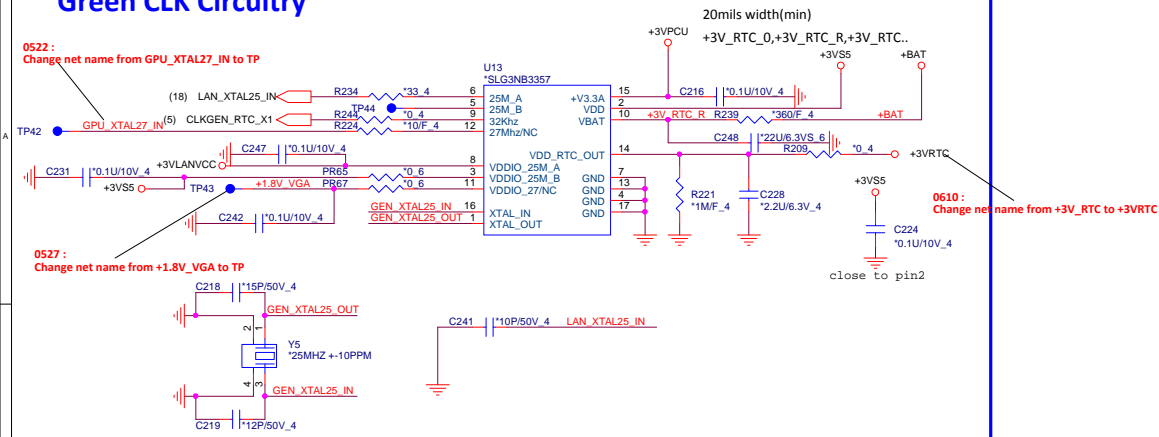


HOLE

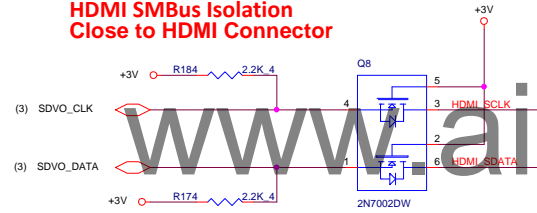


	+3VSS (4,5,6,7,10,13,21,22,24,25,29)
	+5VSS (17,24,25,26,27,29)
	
PROJECT : Y02 Quanta Computer Inc.	
Size Custom	Document Number Hole/Touch PAD I2C
Date: Tuesday, July 02, 2013	Sheet 12 of 30 Rev 1A

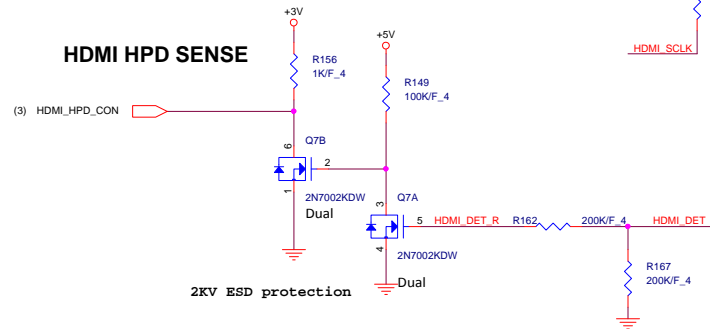
Green CLK Circuitry







HDMI SMBus Isolation Close to HDMI Connector

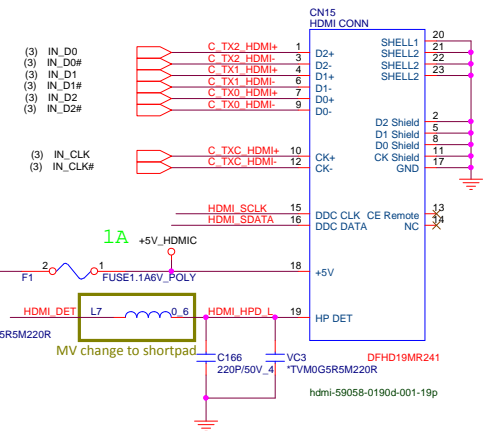
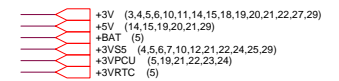


HDMI HPD SENSE



EMI request

C_TX2_HDMI+	R197		120/F_4	C_TX2_HDMI-
C_TX1_HDMI+	R196		120/F_4	C_TX1_HDMI-
C_TX0_HDMI+	R188		120/F_4	C_TX0_HDMI-
C_TXC_HDMI+	R194		120/F_4	C_TXC_HDMI-



PROJECT : Y02
Quanta Computer Inc.

Size Custom	Document Number HDM/GCLK	Rev 1A
Date: Tuesday, July 02, 2013		Sheet 13 of 30

(4) CLK_PCIE_REQ2# CLK_PCIE_REQ2# R253 0.4 CLK_PCIE_REQ2# R

(4) CARD_PCIE_RST# SI change to shortpad
CLK_PCIE_REQ2# R R254 0.4

Zdiff = 100 ohm

- (2) PCIE_TXP3_CARD
(2) PCIE_TXN3_CARD
(5) CLK_PCIE_CARDP
(5) CLK_PCIE_CARDN
(2) PCIE_RXP3_CARD
(2) PCIE_RXN3_CARD

C261 0.1U/10V_4
C260 0.1U/10V_4
PCIE_RXP3_CARD C
PCIE_RXN3_CARD C

Please add 9 GND VIAs
connection with thermal PAD

need colse to Chip

R252 6.2Kf 4

C266 100P/50V_4

C267 0.1U/10V_4

C268 4.7U/6.3V_6

C269 0.1U/10V_4

C270 5.6P/16V_4

C271 1U/10V_4

C272 5.6P/16V_4

C273 5.6P/16V_4

C274 5.6P/16V_4

C275 5.6P/16V_4

C276 5.6P/16V_4

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C467 5.6P/16V_4

C468 5.6P/16V_4

C469 5.6P/16V_4

C470 5.6P/16V_4

C471 5.6P/16V_4

C472 5.6P/16V_4

C473 5.6P/16V_4

C474 5.6P/16V_4

C475 5.6P/16V_4

C476 5.6P/16V_4

C477 5.6P/16V_4

C478 5.6P/16V_4

C479 5.6P/16V_4

C480 5.6P/16V_4

C481 5.6P/16V_4

C482 5.6P/16V_4

C483 5.6P/16V_4

C484 5.6P/16V_4

C485 5.6P/16V_4

C486 5.6P/16V_4

C487 5.6P/16V_4

C488 5.6P/16V_4

C489 5.6P/16V_4

C490 5.6P/16V_4

C491 5.6P/16V_4

C492 5.6P/16V_4

C493 5.6P/16V_4

C494 5.6P/16V_4

C495 5.6P/16V_4

C496 5.6P/16V_4

C497 5.6P/16V_4

C498 5.6P/16V_4

C499 5.6P/16V_4

C500 5.6P/16V_4

C501 5.6P/16V_4

C502 5.6P/16V_4

C503 5.6P/16V_4

C504 5.6P/16V_4

C505 5.6P/16V_4

C506 5.6P/16V_4

C507 5.6P/16V_4

C508 5.6P/16V_4

C509 5.6P/16V_4

C510 5.6P/16V_4

C511 5.6P/16V_4

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C514 5.6P/16V_4

C515 5.6P/16V_4

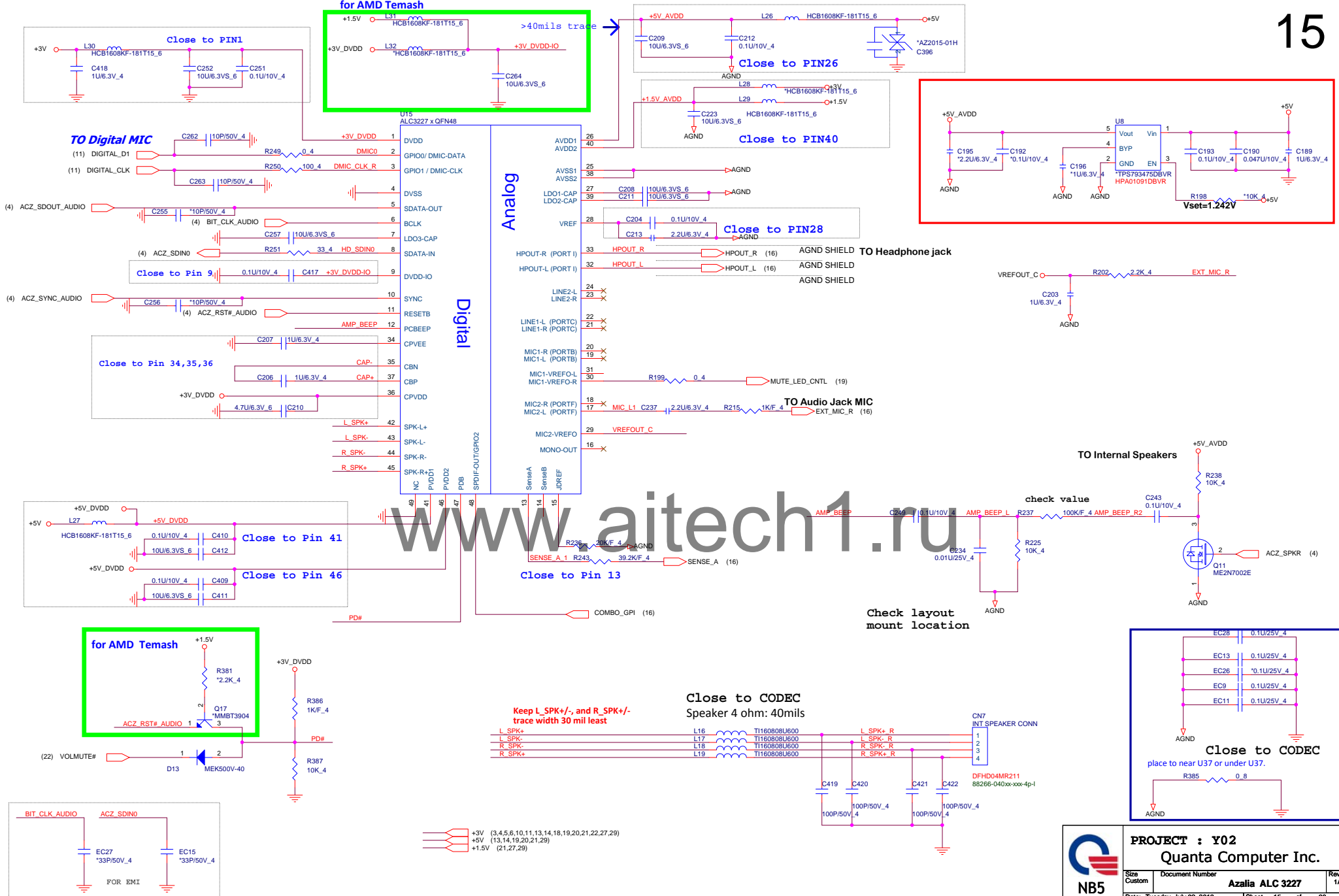
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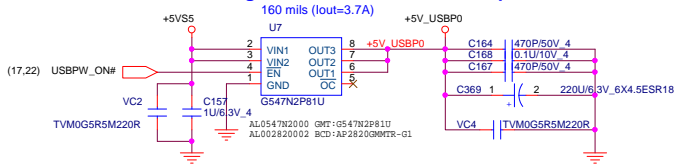
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C520 5.6P/

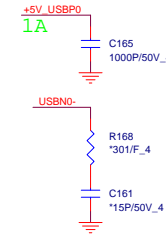
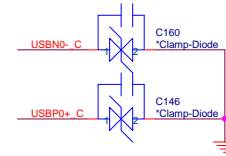
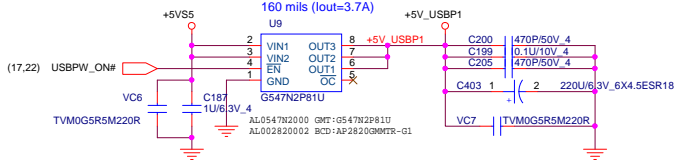




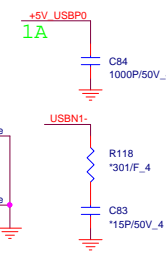
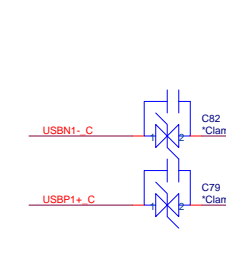
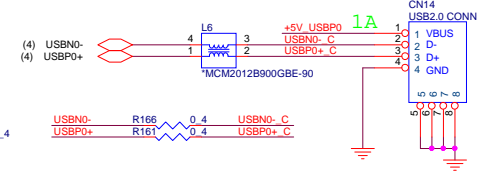
For Right side USB2.0 Connector*2pcs



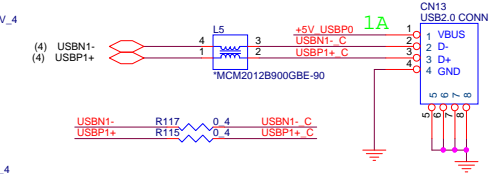
For Left side USB3.0/2.0 Combo *1pcs



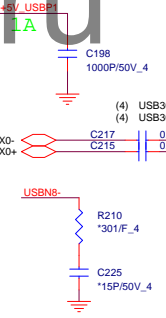
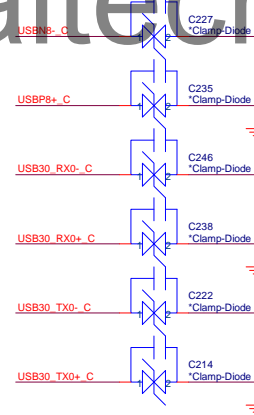
Right side USB2.0 Connector



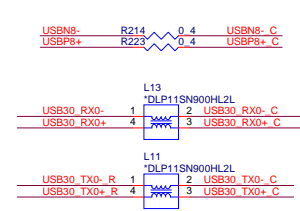
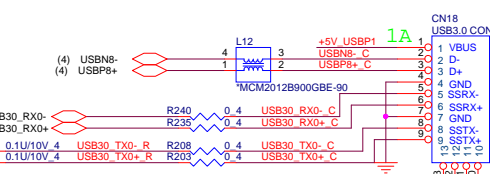
Right side USB2.0 Connector



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Left side USB3.0/2.0 Combo



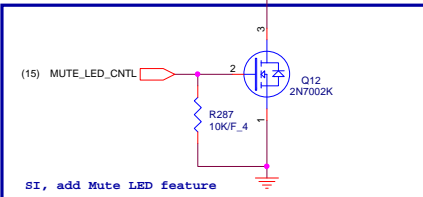
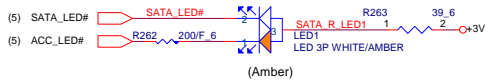
+5VS5 (24,25,26,27,29)

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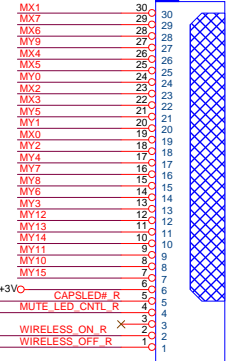
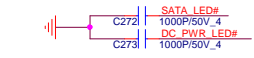
KEYBOARD Con.

(22) MY[0..15] MY[0..15]
(22) MX[0..7] MX[0..7]

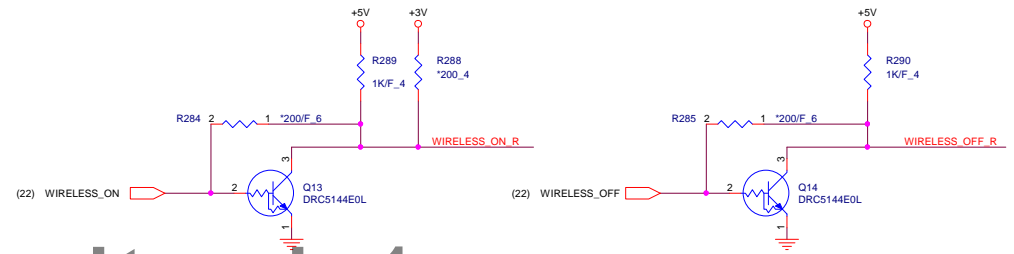
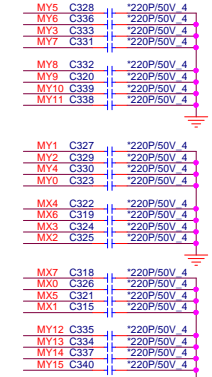
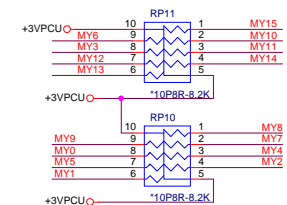
SATA_LED



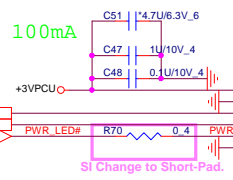
PWR_LED



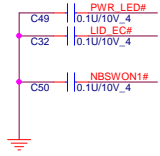
KEYBOARD PULL-UP



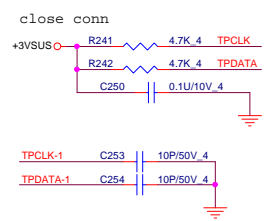
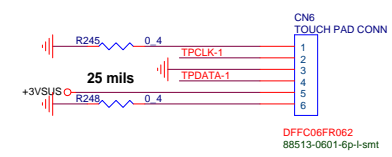
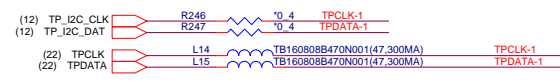
POWER BOTTON CONNECT



1. +3VPCU(LIDSWITCH PWR)
2. GND
3. LIDSWITCH
4. POWERON#
5. PWRLED#
6. GND



TOUCH PAD Con.

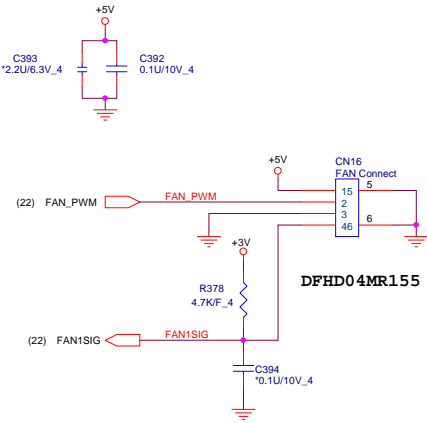


+3V (3,4,5,6,10,11,13,14,15,18,20,21,22,27,29)
+5V (13,14,15,20,21,29)
+3VSUS (29)
+3VPCU (5,13,21,22,23,24)

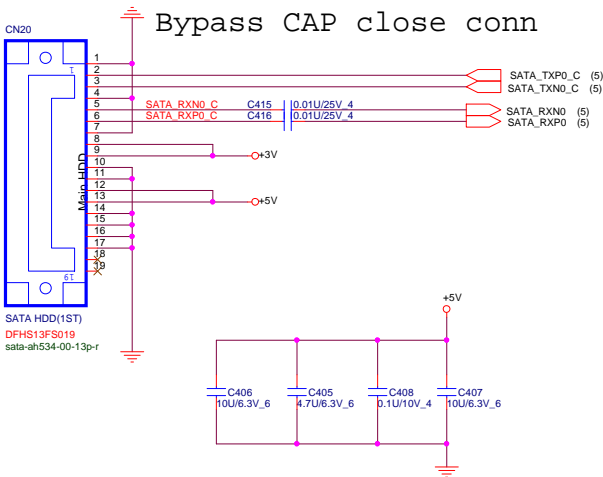
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Quanta Computer Inc.

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CPU FAN




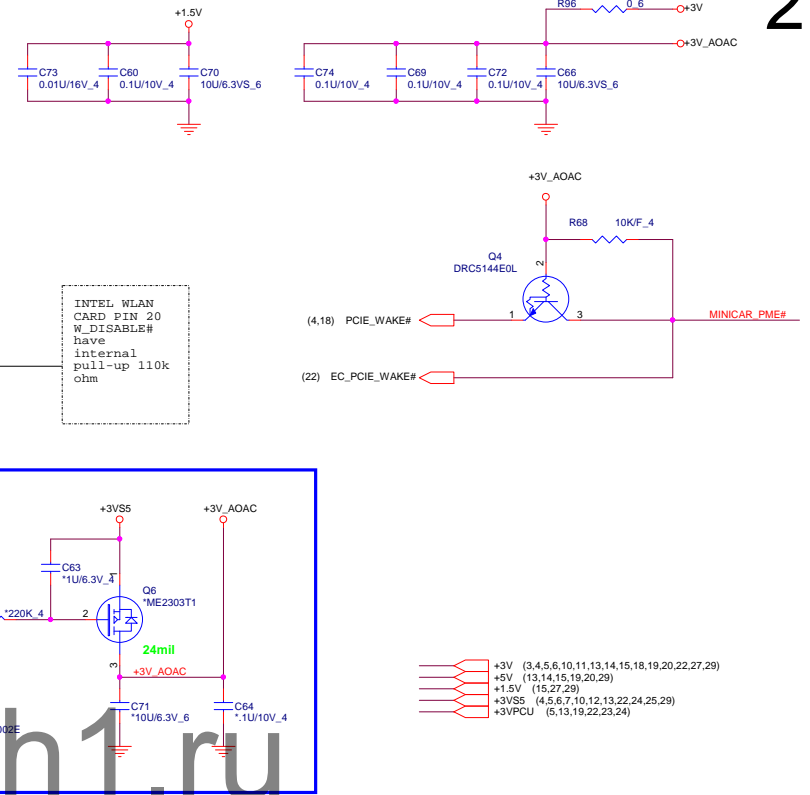
SATA HDD CONNECTOR



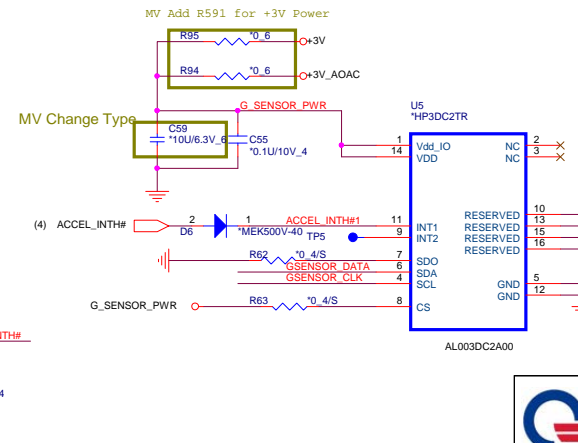
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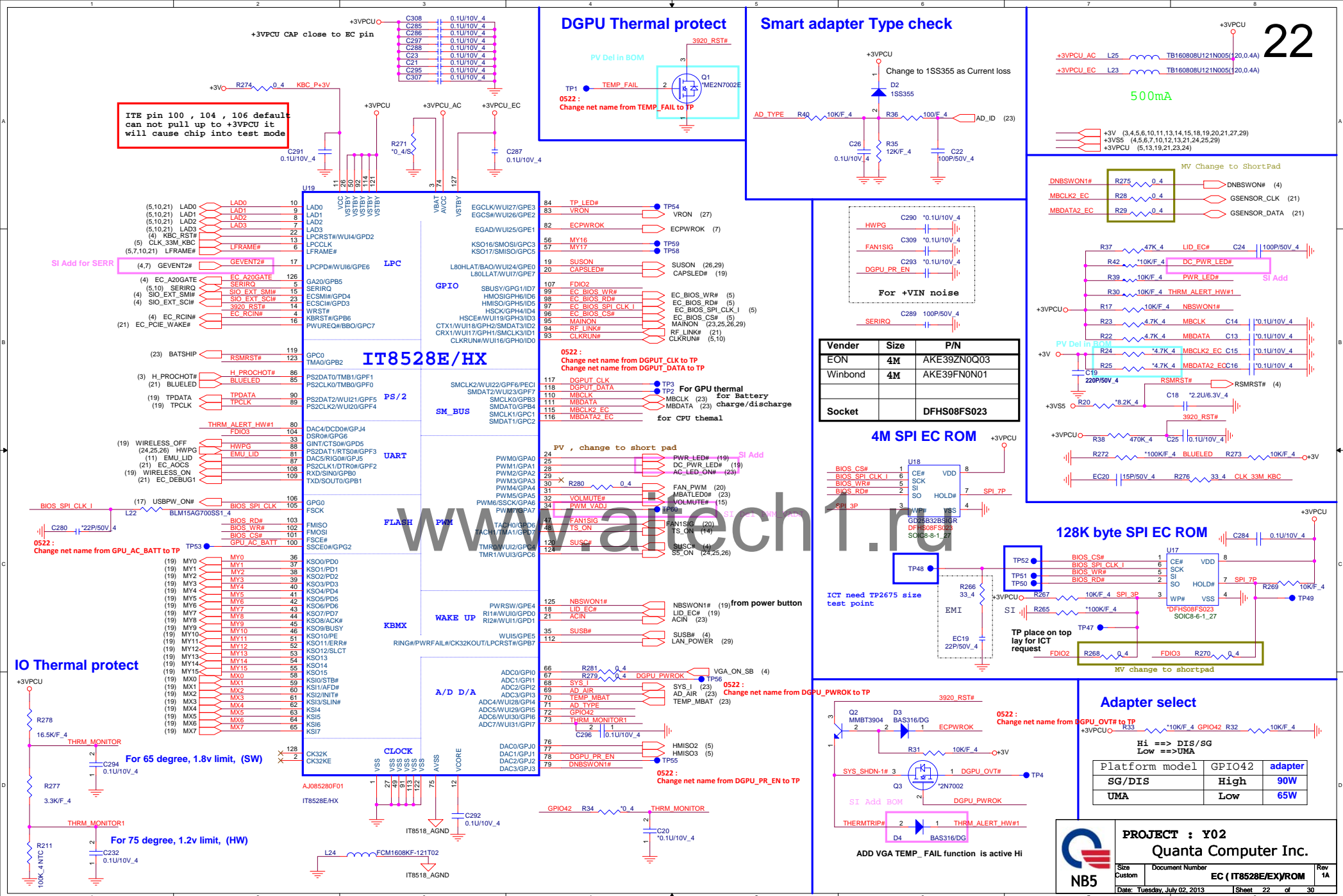
+3V (3,4,5,6,10,11,13,14,15,18,19,21,22,27,29)
+5V (1,3,14,15,19,21,29)

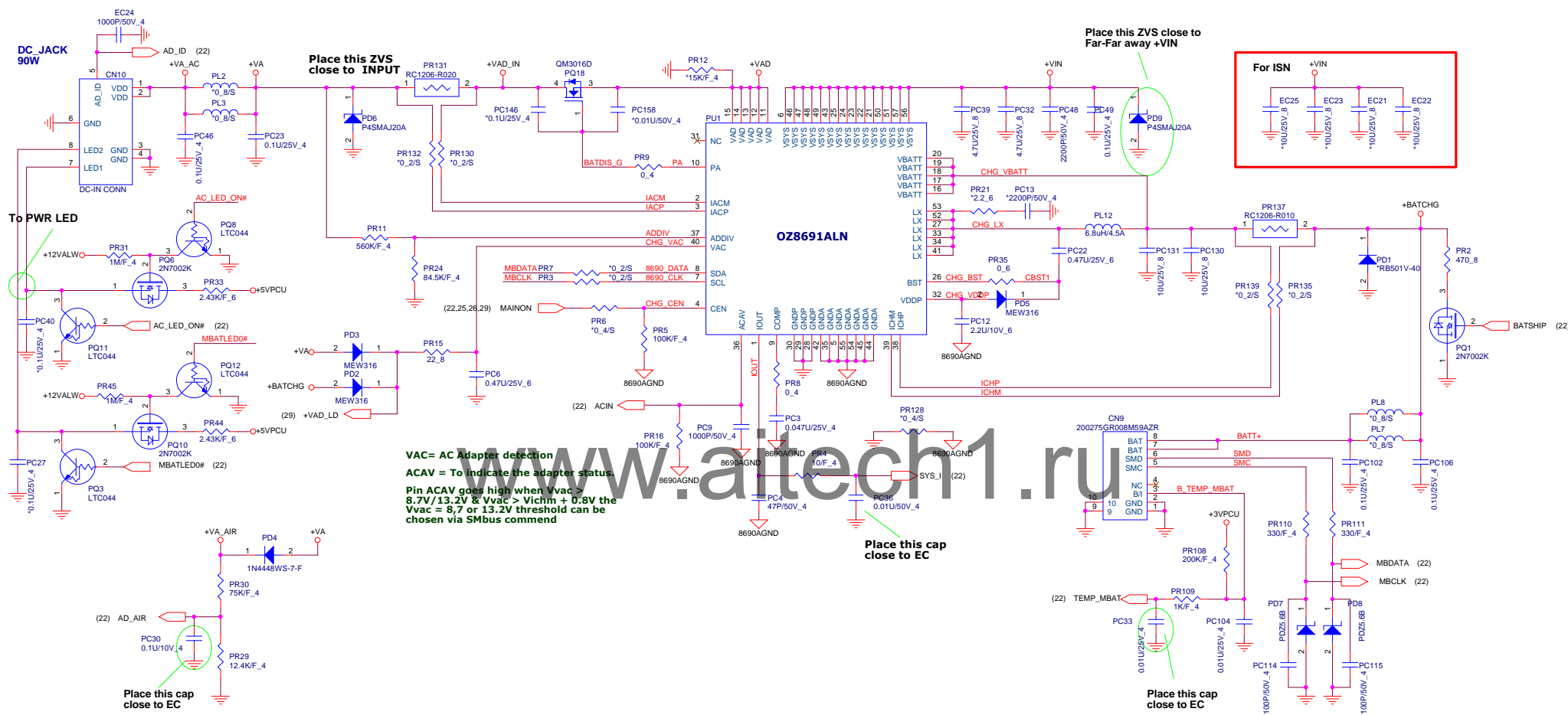
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	Size Custom	Document Number HDD/FAN
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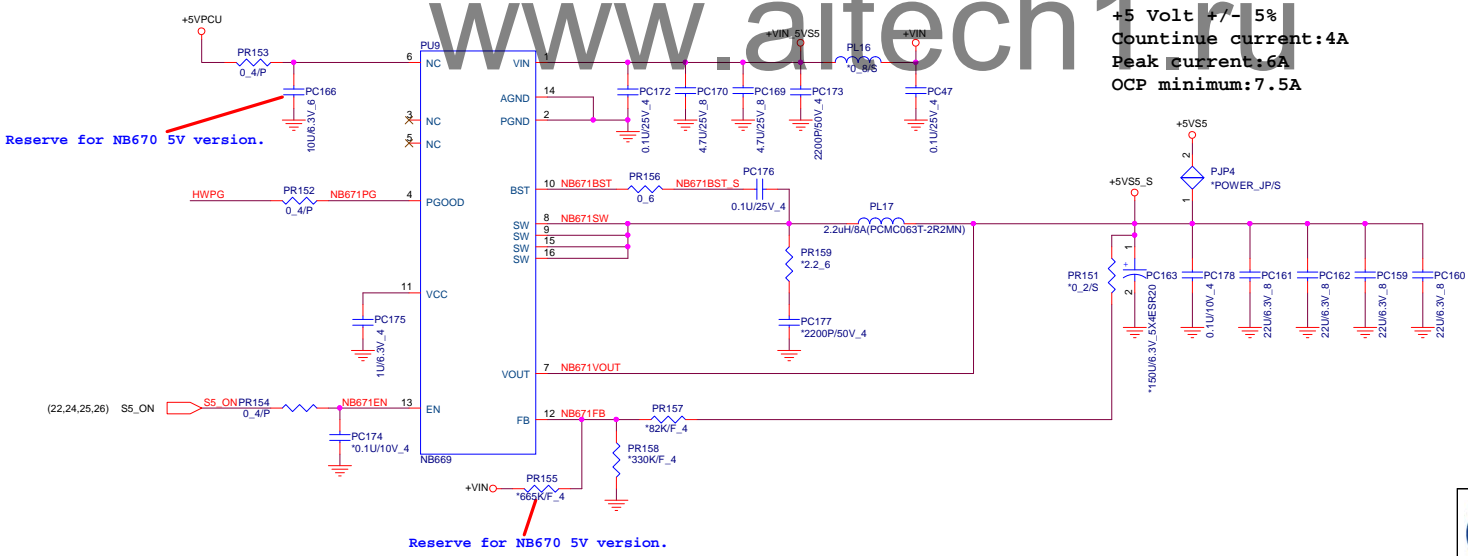
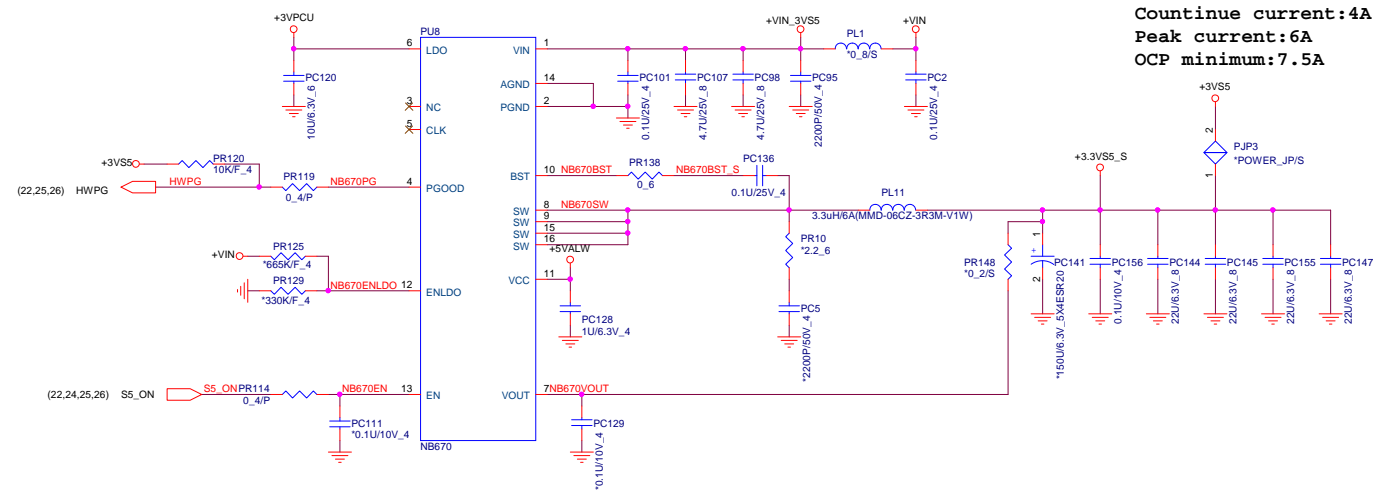



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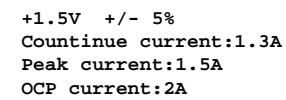
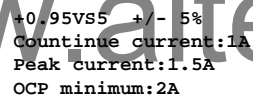
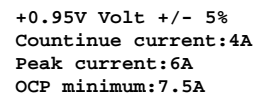


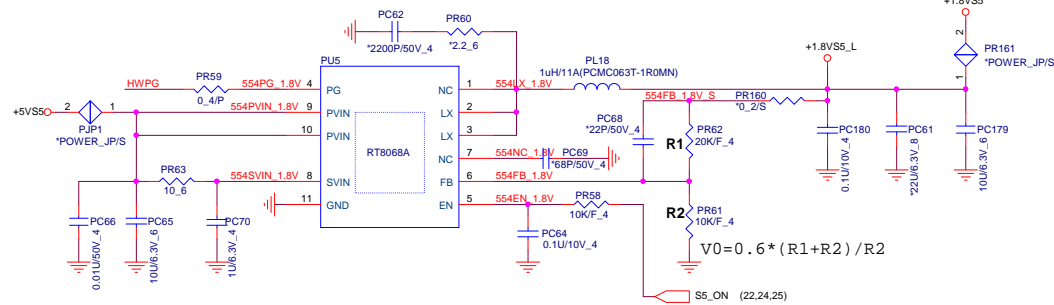
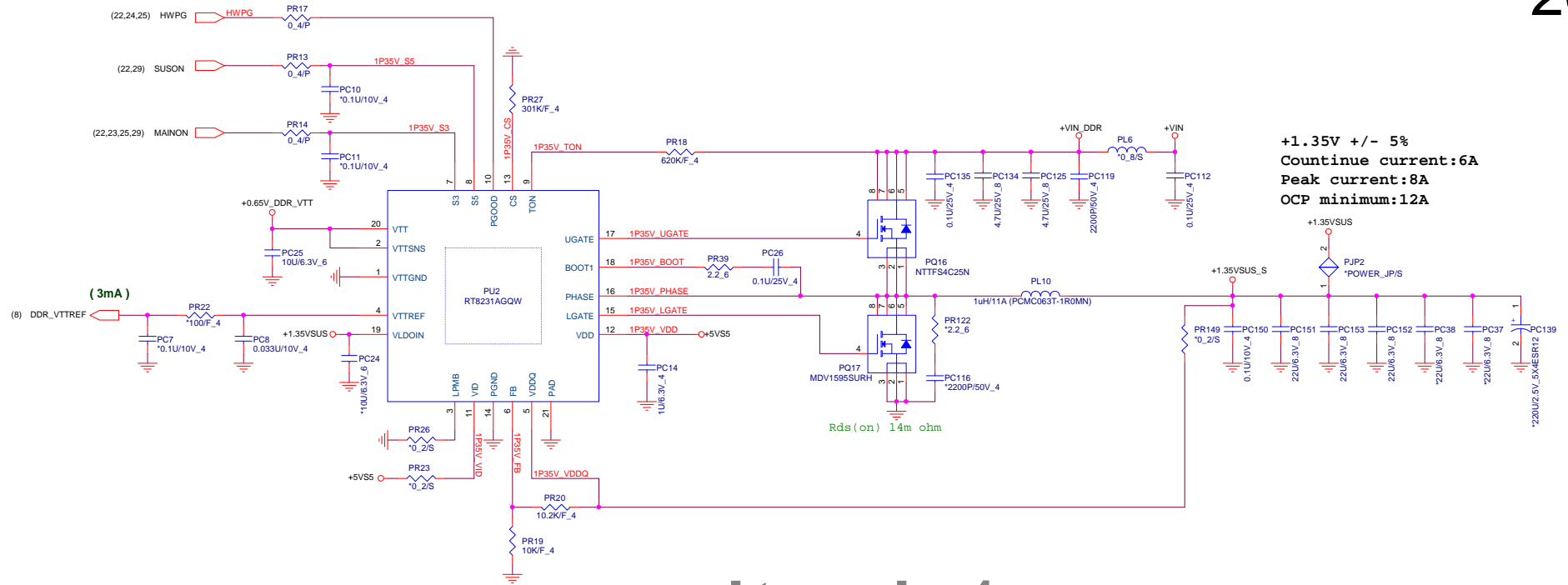


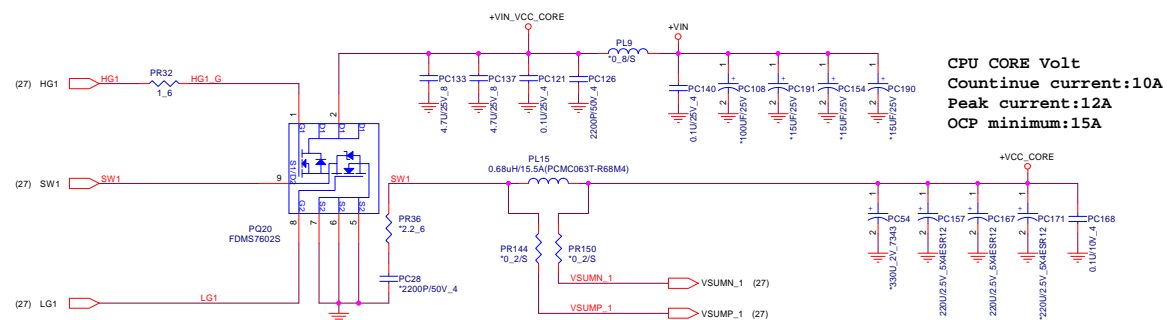




 NB5	PROJECT : Y02 Quanta Computer Inc.			
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